

1/12

Fig. 1

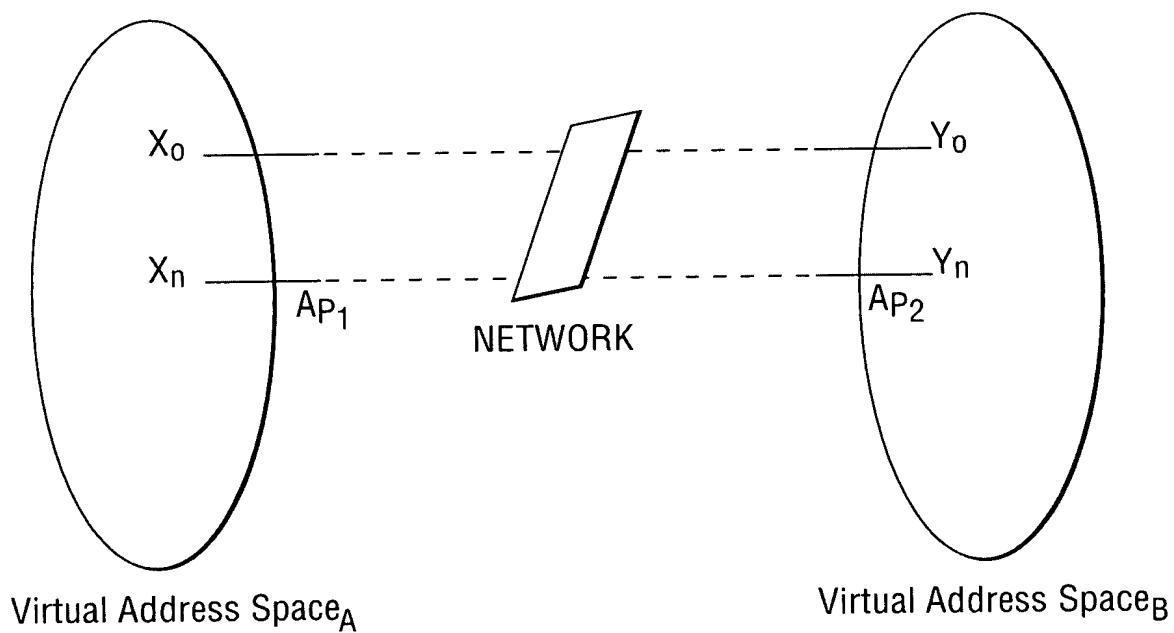
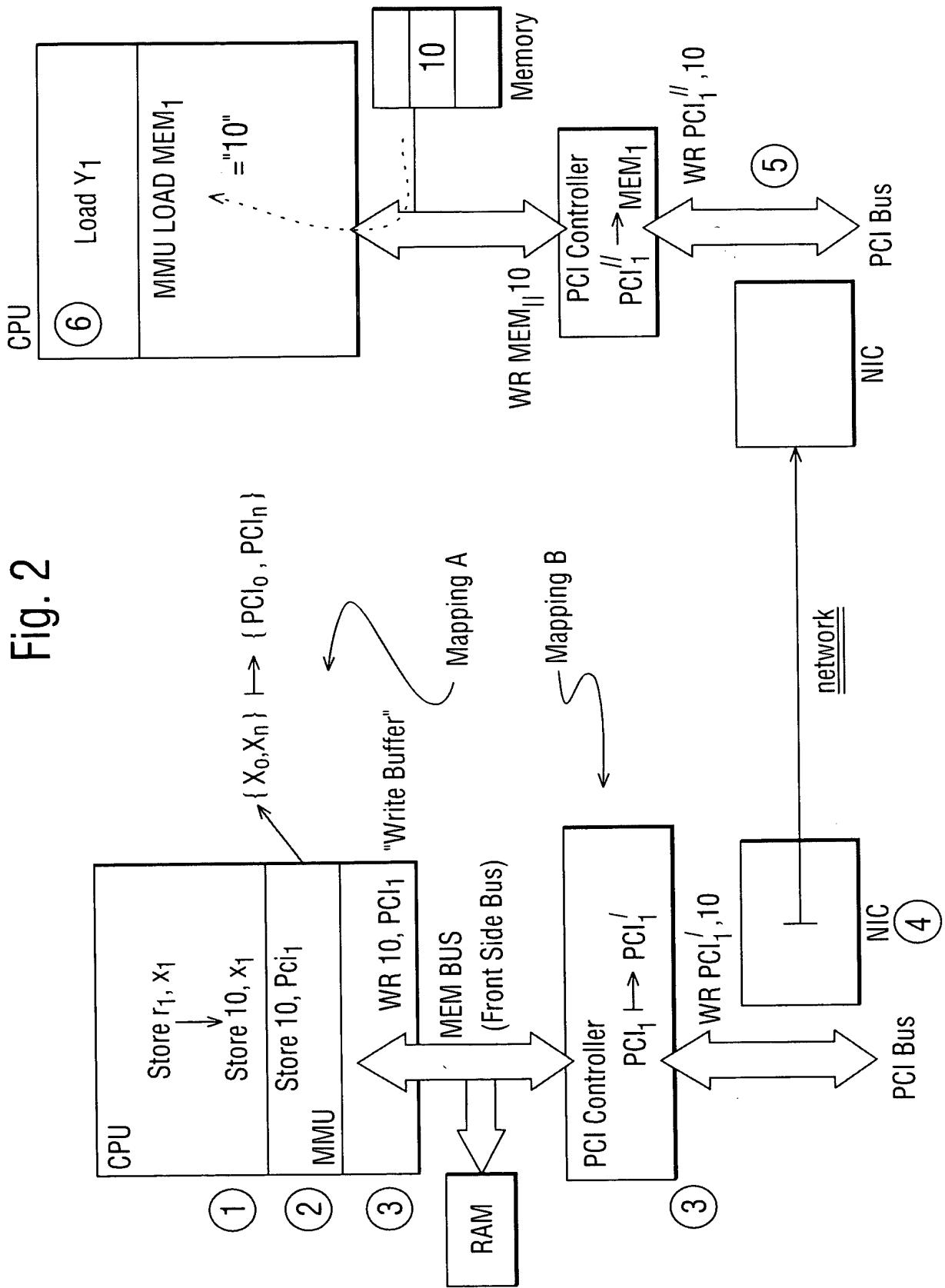


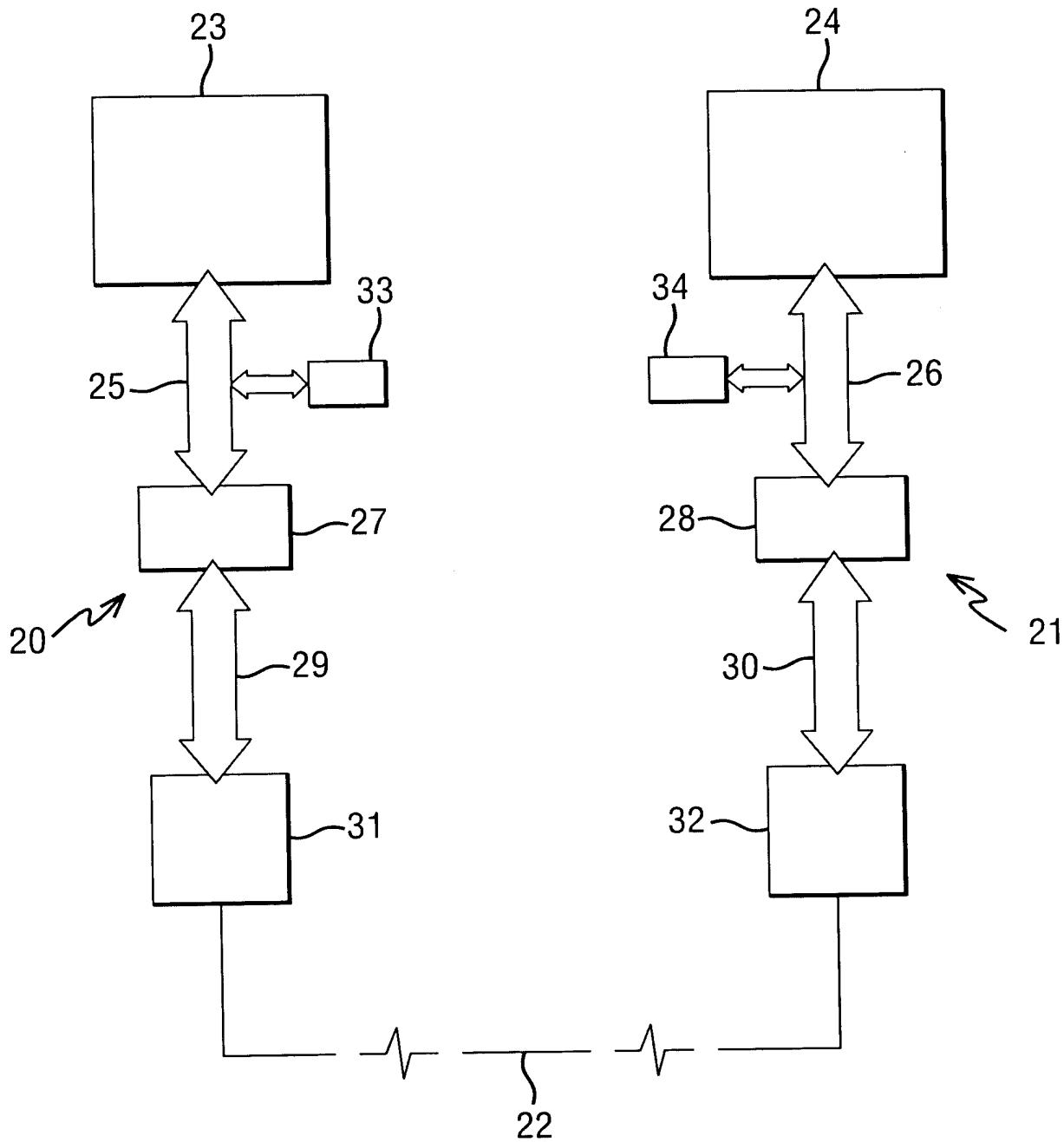


Fig. 2



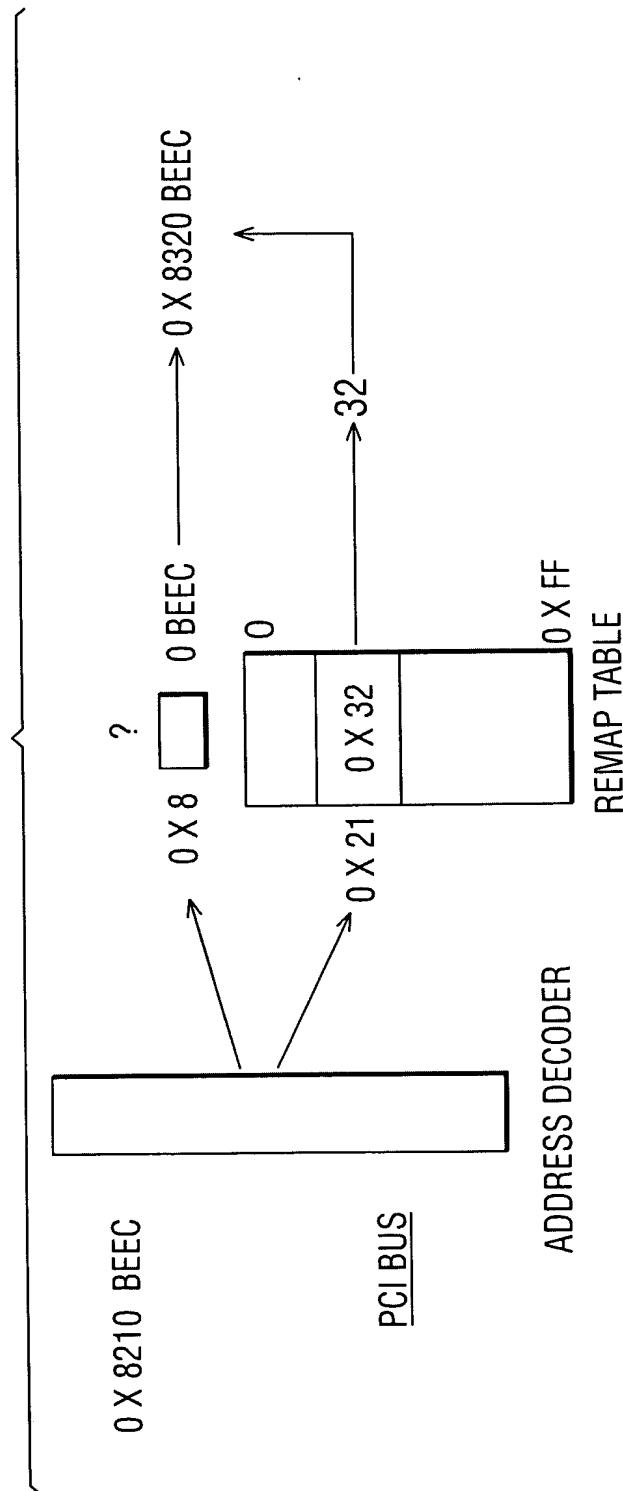
3/12

Fig. 3



4/12

Fig. 4



5/12

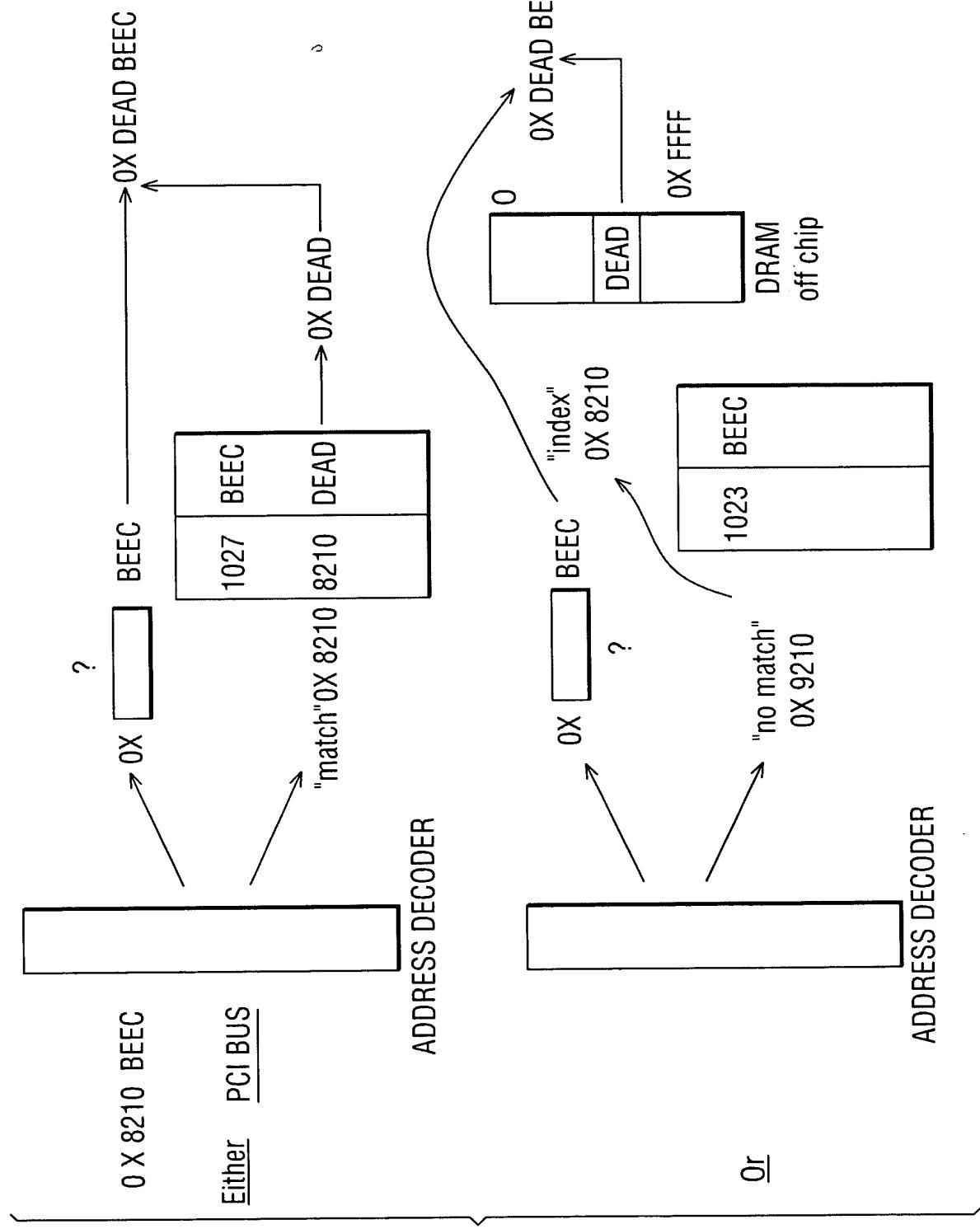


Fig. 5

6/12

Fig. 6

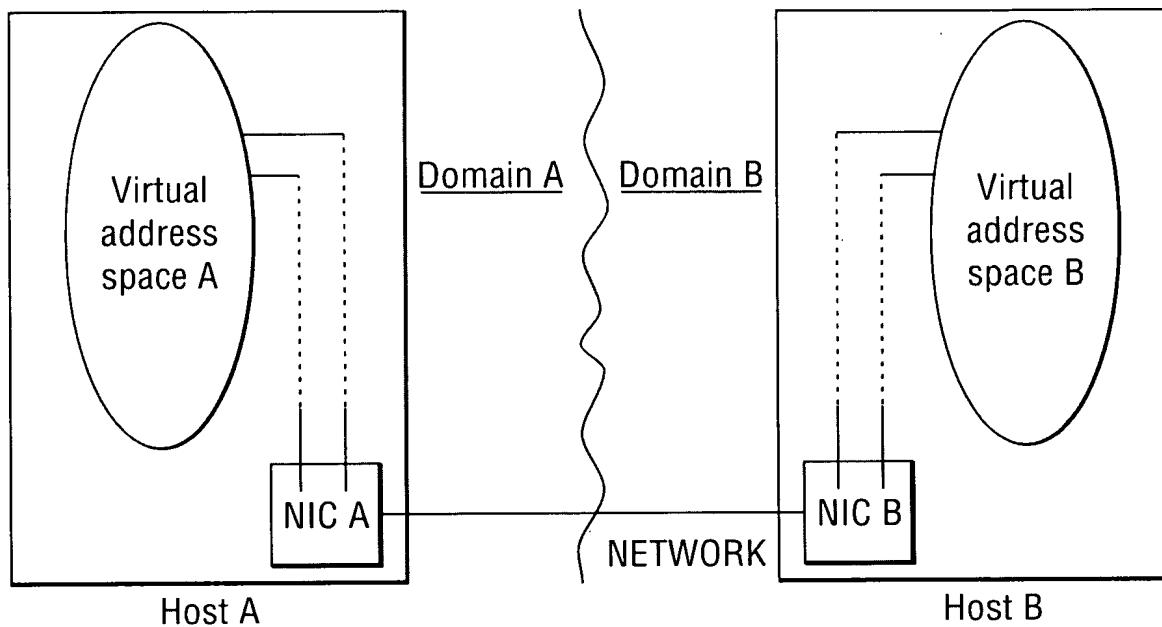
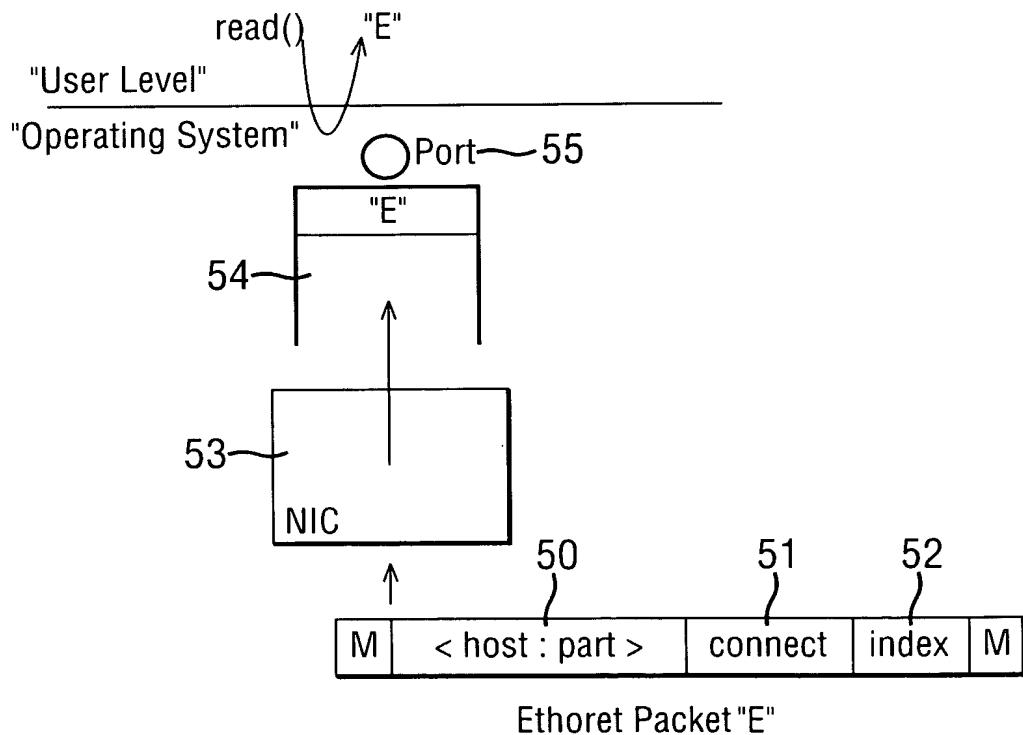
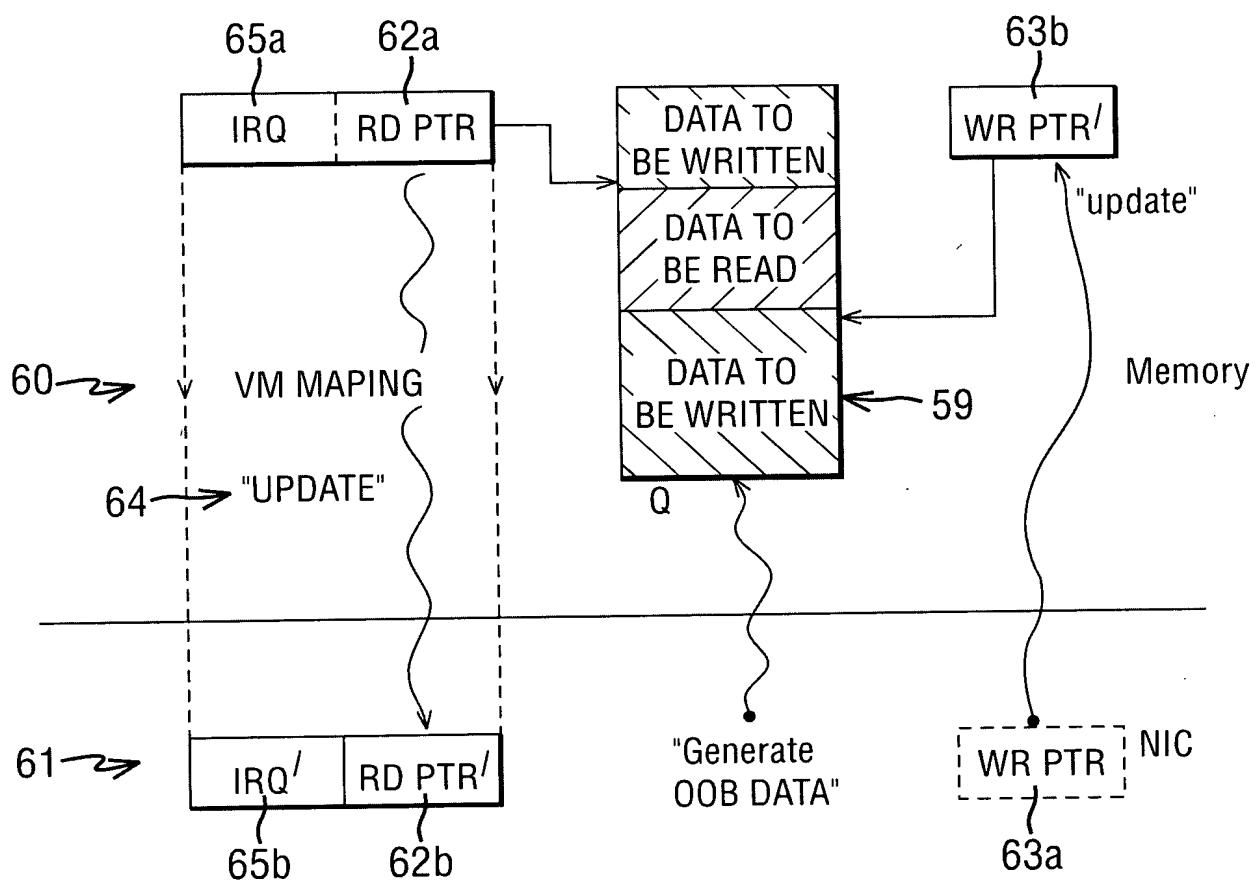


Fig. 7



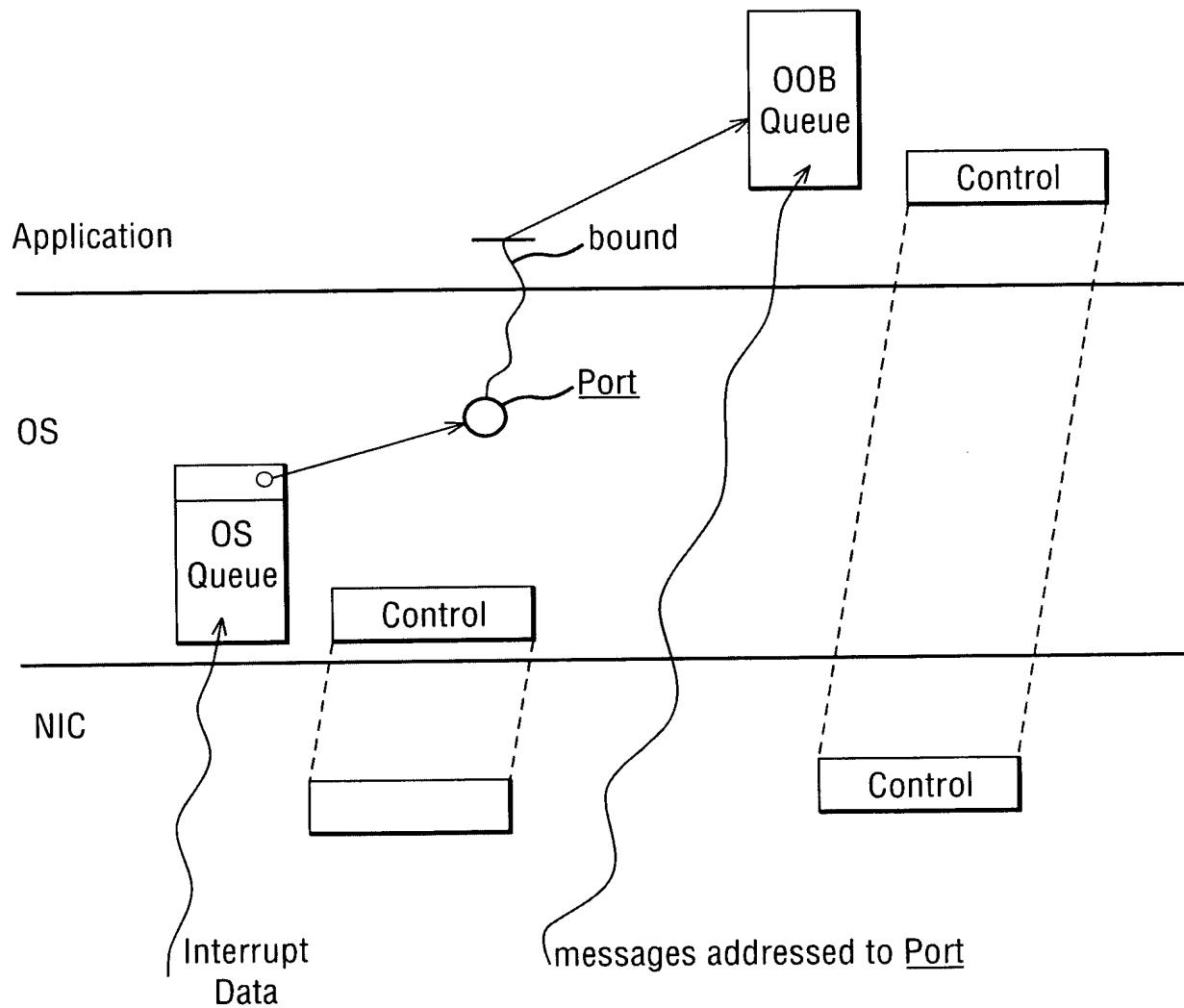
7/12

Fig. 8



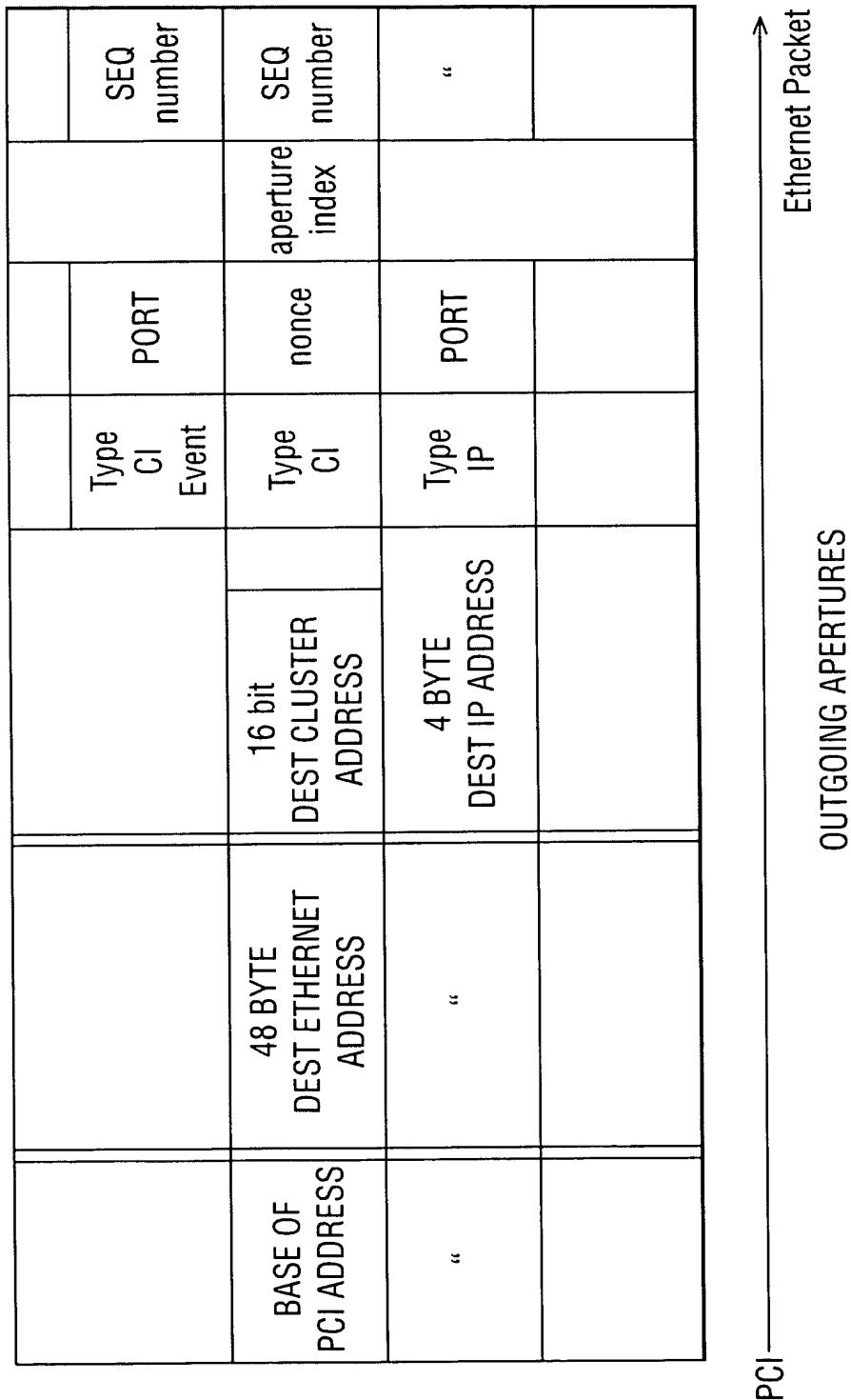
8/12

Fig. 9



9/12

Fig. 10
[NB order here not relevant]



10/12

Fig. 11

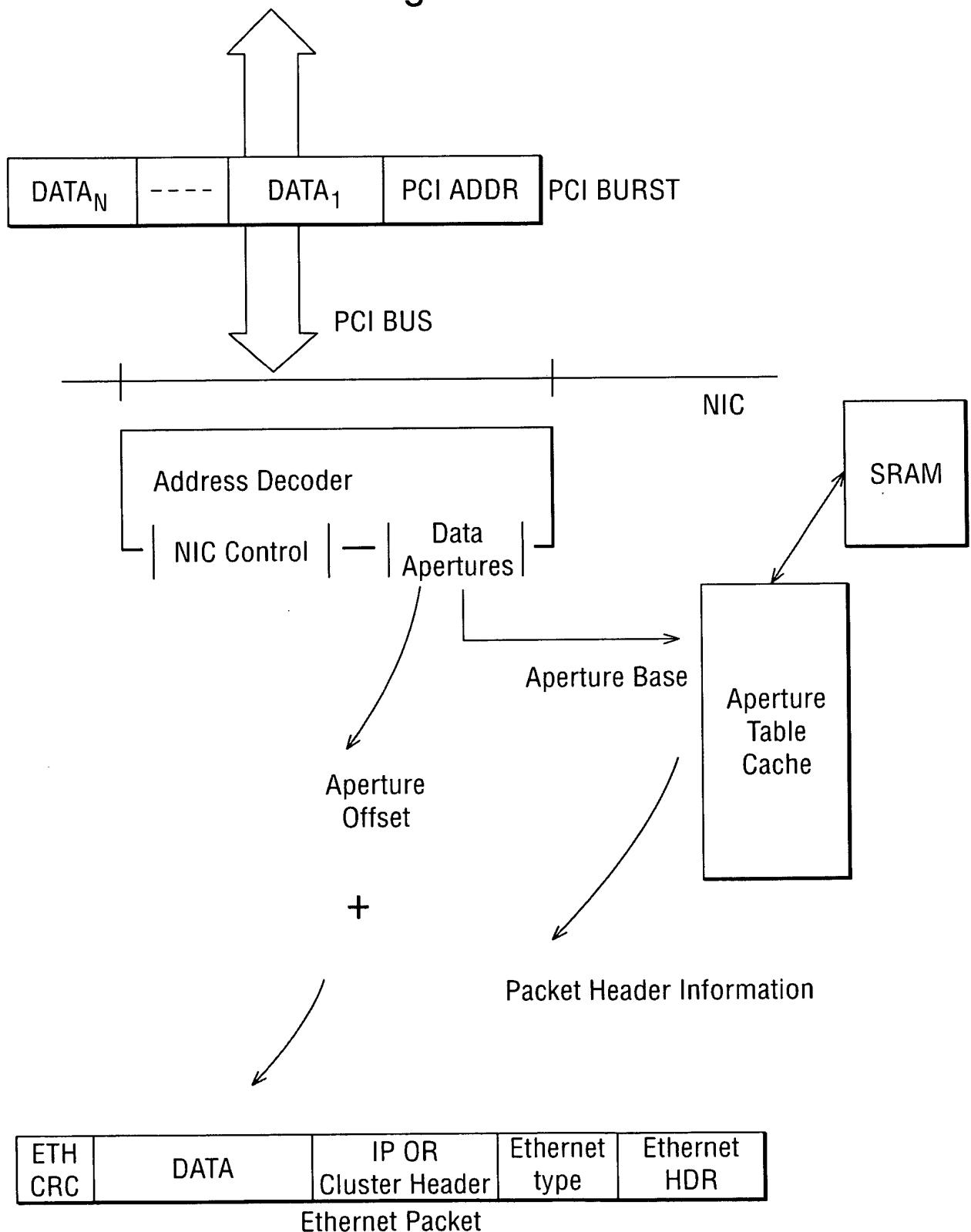
Incoming apertures

Aperture index	nonce	SEQ number	Type CI	Source Cluster Address	Source Ethernet Address	Size	Base of PCI address for aperture
Port		SEQ number	Type CI Event	Source IP Address	Source Ethernet Address		Pointer to descriptor For Event Queue
							Pointer to descriptor variable data queue
				Type IP	"	"	"

Ethernet Packet →PCI

11/12

Fig. 12



12/12

Fig. 13

